

Problem 1: Solve 2021 Final Exam Problem 2(c). In the problem the execution of a loop on a 4-way superscalar MIPS implementation is to be shown.

See posted final exam solution at https://www.ece.lsu.edu/ee4720/2021/fe_sol.pdf.

Problem 2: Solve 2021 Final Exam Problem 1. In this problem some features of an unconventional 2-way superscalar processor are to be completed. The solution to this problem is not as long as it might seem.

See posted final exam solution at https://www.ece.lsu.edu/ee4720/2021/fe_sol.pdf.