LSU EE 4720   **Homework 7**   **Due: 25 April 2018**

Problem 1: Solve 2017 Final Exam Problem 2, which asks for or about pipeline execution diagrams for several code fragments on a superscalar MIPS implementation.

Problem 2: Solve 2017 Final Exam:

(*a*) Problem 5a, comparing superscalar and a scalar processor with a vector unit.

(*b*) Problem 5d, which asks how a VLIW ISA implementation might be simpler than a given superscalar implementation.