

*Note: For help with this and similar assignments see the *Statically Scheduled MIPS study guide* linked to <http://www.ece.lsu.edu/ee4720/guides.html>.*

Problem 1: Solve Spring 2012 Midterm Exam Problem 1. Part a is the usual draw-a-pipeline-execution-diagram-and-find-the-CPI problem, but it's on an implementation with some bypass paths removed. For part b you need to design control logic to generate stalls for the missing bypasses.

Problem 2: Solve Spring 2012 Midterm Exam Problem 2. In that problem the memory stage is split in two.