

EE 2720, Spr. 06, Test 2 review (1)

The test 2 is going to take place on Wed. April 5, 06. It is going to be closed books and closed notes. You are not allowed to use calculators or computers. The materials for which you are responsible are the following:

- Handout #6: Multiplying out and factoring. Here you must also know the theorems from handout #5
- Handout #7: Proving the validity of an equation.
- Handout #8: Principle of Duality; minterms; maxterms; canonical sums; canonical products.
- Handout #9: n -input AND gate; n -input OR gate; n -input NAND gate; n -input NOR gate.

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- Handout # 10: implementation of a logic function using only NAND gates using both the algebraic and graphical approach; implementation of a logic function using only NOR gates using both the algebraic and graphical approach.

- Handout # 11: The 2-input Exclusive OR gate; the 2-input Exclusive NOR gate.

- Handouts # 14, 15, 16: Karnaugh maps. You should know how to provide simplified sum-of-products and simplified product-of-sums with or without don't cares using Karnaugh maps.

END of review