

Wang JianTao

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Date of Birth: Feb,7,1986

Education Background

Major: Bachelor of Engineering. Computer Science,USTC,2005-2009.

Second degree: Bachelor of Business Administration, Management Science,USTC, 2006-2009.

Project and Research

Port UcOs/II to the board of X86: 12/2007-01/2008, winter vacation

Read and analysis the source code of UcOs/II;

Modify the embedded operation system UcOs/II;

Code some application program to satisfy the concrete need;

Modify boot loader for the i386 boards.

My work: Read and analysis the source code;

Design the backend of compiler of MiniJOOL: 04/2008-06/2008, during semester

MiniJOOL is a high level language that resembles JAVA yet simpler than JAVA. Any student can choose the front end or the back end of the compiler as his/her project, no matter which part you choose, you must combine your project (back end or front end) with your partner to a qualified compiler.

My work: Complete the back end with JAVA. During the project, I grasped the AT&T assemble language and JAVA. In addition, I have a deeper understanding about the process of compiling.

Database project: 05/2008-06/2008, during semester

I: Design a Tourism Service System with MFC.

The functions of this system include ticket, hotel, taxi enquiry and reservation. Customers can inquire the current service information of different city and reserve according to the current information.

II: Design a small Database Management System.

This project wants us to design a system like Oracle yet more simple than Oracle. It mainly implements the basic sentences, such as Select, Create, Where, From and so on; In addition, it must check the consistency and integrality . This project should combine with the design of Compiler. It is a good chance for us to integrate what we have learn.

Implement MiniCore's in MIPS version: 07/2008–present, summer vacation

MiniCore, a new-designed operation system by my lab, which is a mature operation system only for i386 platform. And MiniCore is micro-kernel and provides supports of TCP/IP/ICMP net protocol, FAT32, EXT2 file system, PCI bus, Linux running environment, and security.

This project's main goal is that make MiniCore support MIPS architecture.

My work: Design and complete the raw implementation of interrupt and exception. During this project, I master MIPS assemble language and the MIPS architecture.

Community activity and volunteer work

09/2007-08/2008: Commissary in charge of organization of our class.

09/2005-08/2008: Member of student union of School of Information Science and Technology.

09/2005-06/2008: Member of English Club.

10/2005-present: Member of Green Grass Society, which is a student association, serving for the whole school and tutoring children in poverty religions every summer vacation without reward.

Honors & Awards

GPA: 3.46, Rank 23/135

10/2007: Excellent Leader of USTC.

10/2007 The National Encouragement Scholarship

09/2007: Scholarship of Oversea Alumni

09/2008: The National Encouragement Scholarship

References

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