

Problem 1: Note set 1 describes what can be done to evaluate a new microarchitectural feature (and Idea). For the technical report Scott McFarling, “Combining Branch Predictors”, describe which parts of the processor evaluation, as described in note set 1 (see the course home page), were performed.

- (a) Identify variations and configurations simulated.
- (b) Explain what was done for “Idea Performance.”
- (c) Explain what was done for “Concept Performance.”
- (d) Does the paper sufficiently show the value of combined branch predictors? Explain.