

Conference Proceedings

(some of them are as follows)

1. S. Rai and A. Khetrappaul, "Digital revolution in India -A perspective," in proc. of Pacific Telecommunication Conference, (Honolulu,Hawaii, Jan. 13-16, 1985), pp. 275-278.
2. T.Y. Chung, S. Rai, and D.P. Agrawal, "Doubly connected multi-dimensionl regular topolo-gies for MANs and LANs," in proc. Of INFOCOM'88, (New Orleans, March 29-31, 1988), pp. 551-557.
3. A. Kumar, S. Rai, and D.P. Agrawal, "Reliability evaluation algorithms for distributed sys-tems," in proc. of INFOCOM'88, (New Orleans, March 29-31, 1988), pp. 851-860.
4. S. Soh and S. Rai, "Experimental results on preprocessing of path/cut terms in sum of dis-joint products technique," in proc. Of INFOCOM'91, (Miami, Florida), pp. 533-542.
5. C.M. Abdulazeez, S. Rai, "Availability modeling of a sixty four line data PABX system," in proc. of ISCAS '91, (Singapore, June 11-14, 1991), pp. 11-14.
6. J. Trahan and S. Rai, "Reliability evaluation and decision problems in extra stage shuffle-exchange MINs," in proc. of the 11th Int. Conf. on Computer Communica-tion, (Genevo, Italy, Sept. 28-Oct. 2, 1992), pp. 183 - 188.
7. S. Soh, S. Rai, and J.L. Trahan, "Improved lower bounds on the reliability of hypercube ar-chitectures," in proc. of the Fifth ISMM Int. Conf. on Parallel and Distributed Computing and Systems, (Pittsburg, PA, October 1992), pp. 182-187.
8. S. Rai. H. L. Johnson, and V.Ratnam "NEUDEM: Neural network based decision making for generating tests in digital circuits," in 36th Midwest Symp. on Circuits and Systems, (Detroit, Michigan, August 16 - 18, 1993), pp. 596-599.
9. K. Biswas and S. Rai, "Testable realization of CMOS combinational circuits for voltage and current testing," 7th Int. Conf. on VLSI design, (Calcutta, Jan. 5-8, 1994), pp. 197-202.
10. S.Rai, "A direct approach to obtain tighter bounds for large fault trees with repeated events," Rel. and Maint. Symp., (Anaheim, CA, 1994), pp. 475-480.

11. A. Jagannath and S. Rai, "Impact of hardware and software faults on ARQ schemes - An experimental study," Rel. and Maint. Symp., (DC, Jan. 16-19, 1995), pp. 479-485.
12. Y. C. Oh and S. Rai, "A novel architecture for priority handling in an ATM multicast switch", 12th Int. Conf. on Comp. Comm., (Seoul, Korea, August 21-24, 1995). pp. 61-65.
13. A. Bhosekar, S. Rai, and R. Nath, "Redundancy identification using the hyperneural network," in 6th Int. Symp. on IC Tech., Sys. & Appl., (Singapore, Sept. 1995), pp. 60-64.
14. A. D. Raghavendra, S. Rai, and S. S. Iyengar, "Multicast routing in internetworks using dynamic core based trees," IEEE International Phoenix Conference on Computers and Communications, (Phoenix, Arizona, March 1996).
15. Y. C. Oh and S. Rai, "A non-blocking copy network for priority handling in ATM multicast switch," Int. Performance, Computing, and Communication Conference, (Phoenix, Arizona, Feb. 5-7, 1997), pp. 231-237.
16. P. Pancha, M. Veeraraghavan, and S. Rai, "Comparison of video conference realization schemes," IEEE Int. Conf. On Communications, (Montreal, Canada, June 8-12, 1997).
17. R. Kumar, S. Rai, and J. L. Trahan, "Neural network techniques for software quality evaluation," in proc. Rel. and Maint. Symp., (Anaheim, California, Jan. 19-22, 1998), pp. 155-161.
18. S. Rai and Y. C. Oh, "Analyzing packetized voice and video traffic in an ATM multiplexer," Int. Performance, Computing, and Communication Conference, (Phoenix, Arizona, Feb. 16-18, 1998), pp. 367-372.
19. A. Gulati and S. Rai, "Core discovery in Internet multicast routing protocol," Int. Performance, Computing, and Communication Conf., (Phoenix, Arizona, 1999), pp. 143-149.