An EPS version of the MIPS superscalar implementation used in one of the problems below can be found at [http://www.ece.lsu.edu/ee4720/2015/mpipei3ss.eps](http://www.ece.lsu.edu/ee4720/2015/mpipei3ss.eps) and an easy-to-edit Inkscape SVG version can be found at [http://www.ece.lsu.edu/ee4720/2015/mpipei3ss.svg](http://www.ece.lsu.edu/ee4720/2015/mpipei3ss.svg).

**Problem 1:** Solve 2014 Final Exam Problem 2, which asks for control logic in a 2-way superscalar processor.

**Problem 2:** Solve 2014 Final Exam Problem 3, in which branch predictors are analyzed. *Note:* Check earlier final exams for solutions to similar problems.