EE 3950
Electronics
Spring 2007

Instructor: Gabriel A. de Souza
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Office hours: 9:00-10:30 TWTh
12:00-1:00 T

TA: To be announced
Office hours:

Course Description: This course introduces the students with of the design and implementation
of simple electronic circuits, and the terminology in the field thus allowing them to communicate
effectively with electrical engineers.

Prerequisites: EE2950.

Text: David V. Kerns, Jr., and J. David Irwin, Essentials of Electrical and Computer
Engineering, Prentice Hall, 2004

Grading Policy: 2 Tests (10/04 and 11/15), in class: 50%
Final (12/15/07 @ 10:00AM), in class: 30%
Homeworks: 10%
Quizzes: 10%

At the discretion of the instructor, a grading curve may be applied to each exam.

Policy on Exams: Make-ups on Exams are allowed under extenuating circumstances, and they
may be oral or written. All corrections to grading must be made within two weeks of exam return.

Policy on Quizzes and Homework: Make-ups on Quizzes are allowed under extenuating
circumstances, and they may be oral or written. The student will be given the opportunity to submit late homework until
the first class after the homework’s due date. Late homework will be penalized 10% of the original maximum
grade for each day it is late. Assignments are due at the beginning of class. All corrections to grading must be made
within two weeks of assignment return.