Instructor: Gabriel A. de Souza
Room 313 EE Building, Ph: 578-4831, email: gdesou1@lsu.edu
Office hours: 9:50-12:00 MTWTh
1:20-2:20 TTh

Course Description: This course introduces the student to the theory and design of Microprocessors, semiconductor technologies, architectures, assembly language, software development, input/output design, applications, and interfacing.

Prerequisites: CSC1253 and EE2730


Grading: 2 Tests (06/27, 07/18): 60%
Final (08/01 @ 4:00PM): 30%
Homework/Quizzes: 10%

At the discretion of the instructor, a grading curve may be applied to each exam.

Policy on Exams: Make-ups on Exams are allowed under extenuating circumstances, and they may be oral or written. At the discretion of the instructor a grading curve may be used to allow for a more favorable grade distribution. All corrections to grading must be made within two weeks of exam return.

Policy on Quizzes and Homework: There will be no make-up quizzes or homework due to the fact that one or more of the lowest grades will be dropped. The student will be given the opportunity to submit late homework until the first class after the homework’s due date. Late homework will be penalized 10% of the original maximum grade for each day it is late. Assignments are due at the beginning of class. All corrections to grading must be made within two weeks of assignment return.