

**Department of Electrical and Computer Engineering
Louisiana State University
EE 3220 Electronics II Spring 2004**

Schedule: 10:30-11:30, MWF **Location:** 2161 CEBA

Catalog Description: EE 3220 Electronics II (3) Prereq: EE 2130, 2230, 2231 (C or better)

Course Objective: To train students in the function and operation of electronics laboratory instrumentation; to illustrate theoretical ideas in a practical manner; and to practice a modest amount of technical communication.

Text: Mark N. Horenstein, Microelectronic Circuits and Devices, Second Edition, Prentice Hall 1996

Instructor: John Scalzo, MSEE Georgia Tech, BSEE Virginia Tech
Electrical Engineering Building 102 F
Phone: 578-5481 **e-mail:** sbjscalz1@lsu.edu
Experience: Microelectronics Components Design Engineer, RF Engineer
Specialization: DSP, Analog and Digital Communications, and Automata Theory.
Courses include Coding Theory, Satellite communications, Cellular communications, Digital Speech processing, Digital Image Processing, Frequency Synthesizers, RF Engineering

Office Hours: M 9:00-10:00 am/T 9:00-10:00,12:30-1:30 pm/Th 1:30-2:30 pm/F 8:30-9:30 am

Prerequisites by topic:

1. Semiconductor device operation
2. Loop & node analysis
3. Sinusoidal analysis
4. Complex frequency techniques
5. Elementary differential equations

Topics:

1. Small-Signal equivalent circuits, two-port analysis, low frequency amplifier circuit analysis and design (Chapter 7)
2. Multi-transistor amplifier analysis and design (Chapter 8)
3. Frequency response of single and multiple transistor amplifier analysis and design (Chapter 9)
4. Feedback and Stability (Chapter 10)
5. Active Filters and Oscillators (Chapter 13)
6. Multistage and power amplifiers (Chapter 11)
7. Digital Electronics (Chapter 14)

Grading:	Homework	10%	
	PSPICE	10%	
	Quizzes	5%	
	Test 1	20%	
	Test 2	20%	
	Final Exam	35%	Wednesday May 12, 5:30-7:30 pm

The final exam is cumulative and will replace the lowest test grade, if it helps.

Missed exams and assignments: Make-up tests will be given only for excused absences (illness, family emergency). Students must notify me immediately upon knowing that a test will be missed. Test 1 must be made up before test 2. Test 2 must be made up before the final exam. Exams that are not taken will not be replaced by the final exam grade. Late homework will be accepted, but will be penalized 1 letter grade per day late. Assignments are due at the beginning of class.

Academic Dishonesty: Academic dishonesty of any kind will not be tolerated. Anyone suspected of cheating will be reported to the Dean of Students.

Attendance and Tardiness: There will be no attendance or tardiness policies for this class.