

Baugh-Wooley multipliers

(1) j

$$\left. \begin{array}{l} A = (a_{m-1} a_{m-2} \dots a_1 a_0)_2 \\ B = (b_{n-1} b_{n-2} \dots b_1 b_0)_2 \end{array} \right\} \begin{array}{l} \text{Signed} \\ \text{numbers} \end{array}$$

Then

$$\begin{aligned} P = A \times B &= a_{m-1} \cdot b_{n-1} \cdot 2^{m+n-2} \\ &+ \sum_{i=0}^{m-2} \sum_{j=0}^{n-2} a_i b_j 2^{i+j} \\ &+ 2^{m-1} \cdot (-2^n + 2^{n-1} + \overline{a_{m-1}} \cdot 2^{n-1} + a_{m-1} \\ &\quad + \sum_{j=0}^{n-2} a_{m-1} \cdot \overline{b_j} \cdot 2^j) \\ &+ 2^{n-1} \cdot (-2^m + 2^{m-1} + \overline{b_{n-1}} \cdot 2^{m-1} + b_{n-1} \\ &\quad + \sum_{i=0}^{m-2} b_{n-1} \cdot \overline{a_i} \cdot 2^i) \end{aligned}$$

(1)

(2) j

The only negative summands in equation (1)
are

$$\begin{aligned} & 2^{m-1}(-2^n + 2^{n-1}) + 2^{n-1}(-2^m + 2^{m-1}) \\ &= 2^{m-1}(-2^{n-1}) + 2^{n-1}(-2^{m-1}) = \\ &= -2^{m+n-2} - 2^{m+n-2} = -2^{m+n-1} \end{aligned} \quad (2)$$

Since $P = A \times B$ is an m -by- n signed product,
the length of P is $m+n$ bits. The
leftmost bit of the product is its sign
bit which has a negative weight factor
of -2^{m+n-1} . Thus, the term -2^{m+n-1}
of the right handside of (2) corresponds to
a +1 at the location of the sign bit
(or the column-location $m+n-1$).

(3) j

Example: Construct a 6-by-4 Baugh Wooley multiplier.

Solution: Here $m = 6$, $n = 4$

$$\begin{array}{r}
 a_5 \quad a_4 \quad a_3 \quad a_2 \quad a_1 \quad a_0 = A \\
 \times \qquad \qquad \qquad b_3 \quad b_2 \quad b_1 \quad b_0 = B \\
 \hline
 a_4b_0 \quad a_3b_0 \quad a_2b_0 \quad a_1b_0 \quad a_0b_0 \\
 a_4b_1 \quad a_3b_1 \quad a_2b_1 \quad a_1b_1 \quad a_0b_1 \\
 a_5b_3 \quad a_4b_2 \quad a_3b_2 \quad a_2b_2 \quad a_1b_2 \quad a_0b_2 \\
 \overline{a_5} \quad \overline{a_5b_2} \quad \overline{a_5b_1} \quad \overline{a_5b_0} \\
 1 \quad \overline{b_3} \quad \overline{a_4}b_3 \quad \overline{a_3}b_3 \quad \overline{a_2}b_3 \quad \overline{a_1}b_3 \quad \overline{a_0}b_3
 \end{array}$$

(4)j

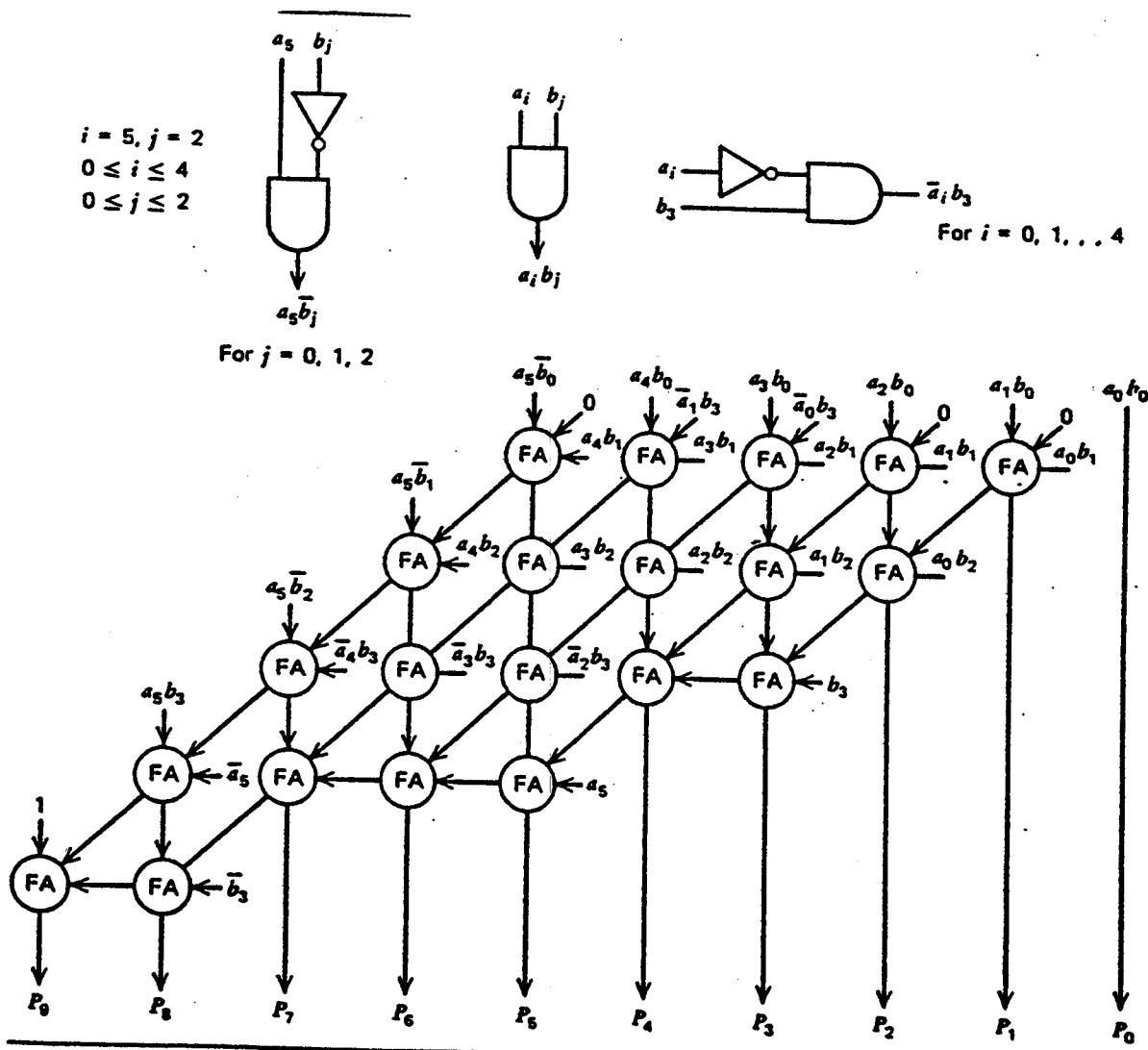
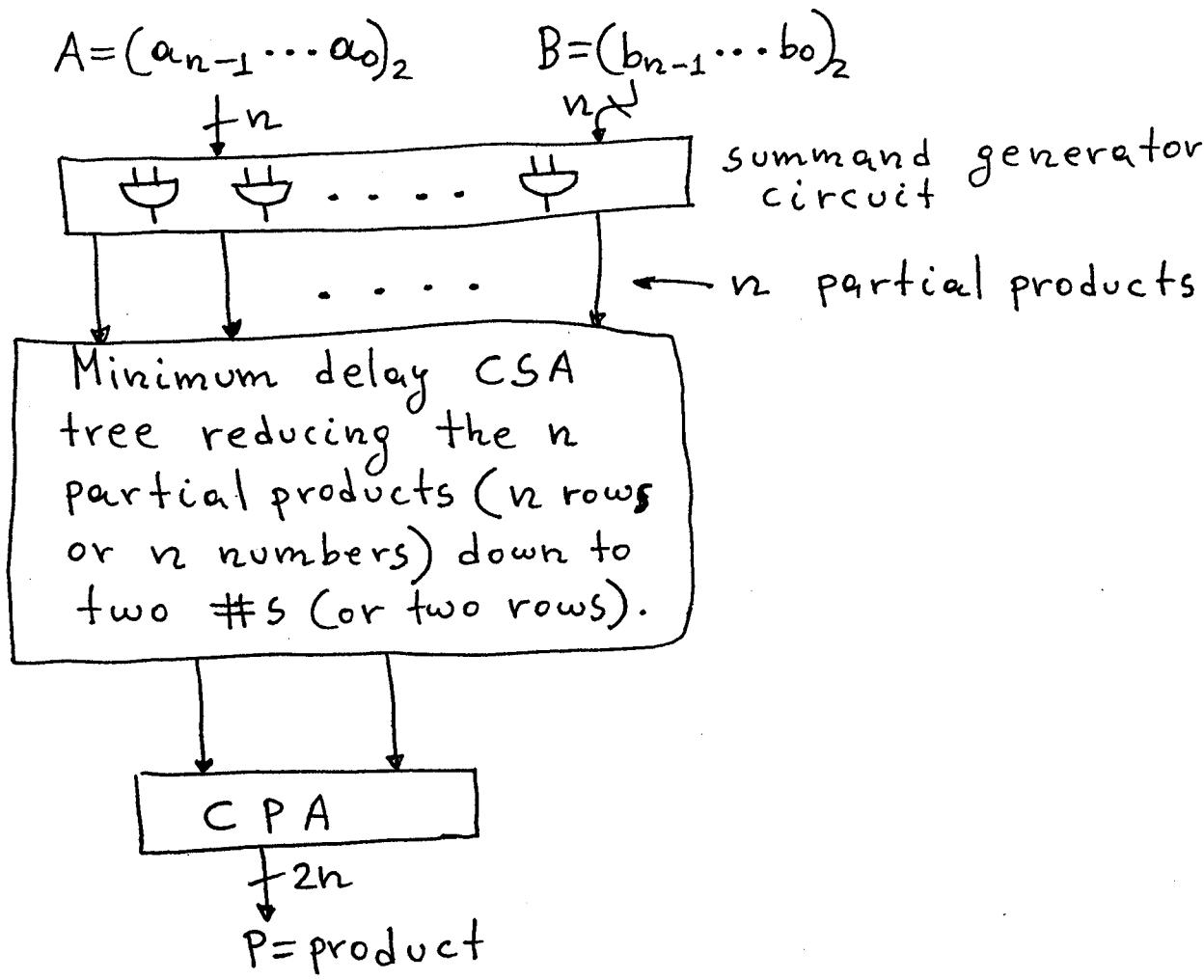


Figure 6.16 The schematic logic circuit diagram of a 6-by-4 Baugh-Wooley two's complement array multiplier.

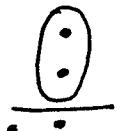
Minimum delay unsigned multipliers

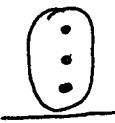
based on counters.



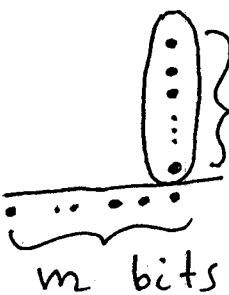
The CSA tree can consist of counters like $(2,2)$ counters or Half Adders (HA), $(3,2)$ counters or Full Adders (FA), or in general (k,m) counters.

(2) k

(2,2) counter  ← reduces the column height by one.

(3,2) counter  ← reduces the column height by two.

In general

(k,m) counter  $\{$ k bits ← reduces the column height by $k-1$.
m bits

Question: What is the minimum number of levels of counters that need to be involved in the reduction down to two rows (two numbers)? In other words, how many levels will the minimum delay CSA tree have?

Answer: The answer to the above question can be obtained from the equations relating the maximum # of operands $\Theta(v)$ and the # of levels v for a minimum delay CSA(k, m) trees.

(3) k

For example, if a CSA(3,2) tree is to be used consisting of (3,2) counters, (sometimes (2,2) counters can be used if (3,2) counters are not needed), then

$$\Theta(v) = \left\{ \begin{array}{l} \left\lfloor \frac{\Theta(v-1)}{2} \right\rfloor \times 3 + (\Theta(v-1)) \bmod 2 \\ \Theta(1) = 3 \end{array} \right. \quad (1)$$

The sequence of $\Theta(v)$ numbers obtained from (1) is

$$3, 4, 6, 9, 13, 19, \dots$$

It is the above sequence of numbers that dictates the number of levels of counters involved in the reduction.

(4) k

Problem 1: Reduce the matrix of summands of a 16-by-16 unsigned multiplier down to two rows using (3,2) and (2,2) counters. Minimum number of levels and minimum amount of hardware should be used. The sequence of numbers used for such reductions is 3, 4, 6, 9, 13, 19, ...

Solution: The matrix of summands of a 16-by-16 unsigned multiplier consists of 16 partial products (16 rows or 16 numbers). Since

$$13 < 16 < 19$$

) six levels of reduction involving (3,2) and (2,2) will be needed.

1st level is 16 down to 13 reduction

2nd level is 13 down to 9 reduction

3rd level is 9 down to 6 reduction

4th level is 6 down to 4 reduction

5th level is 4 down to 3 reduction

6th level is 3 down to 2 reduction

The reductions follow on the next pages.

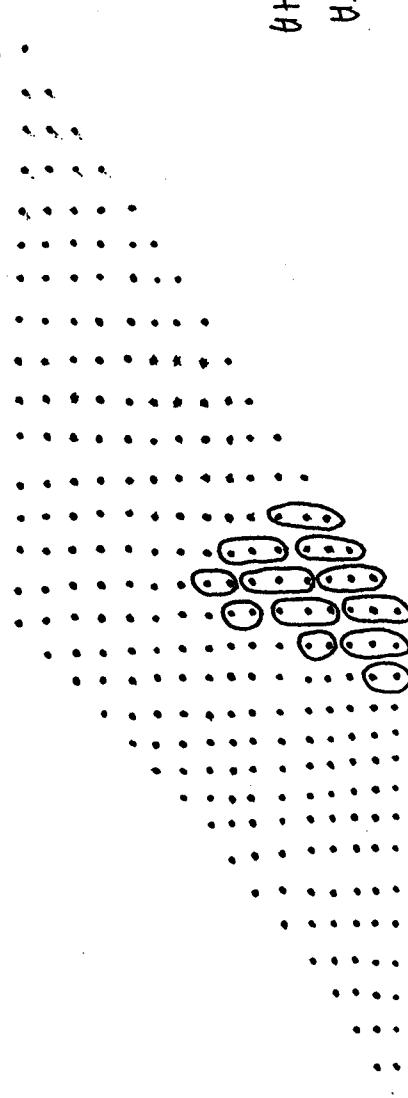
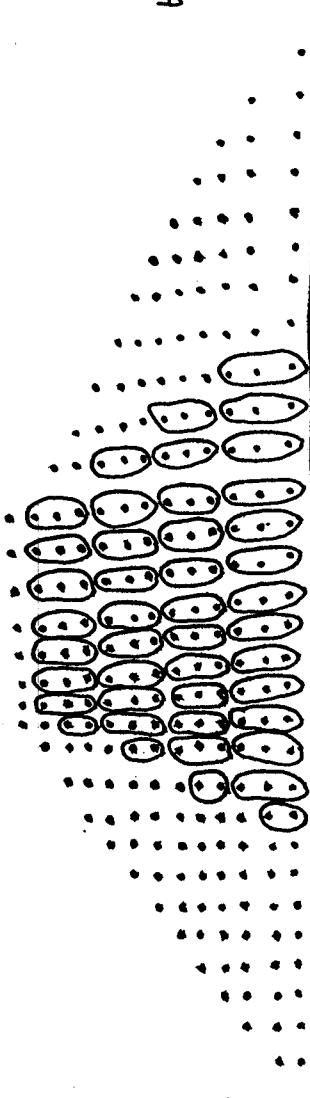
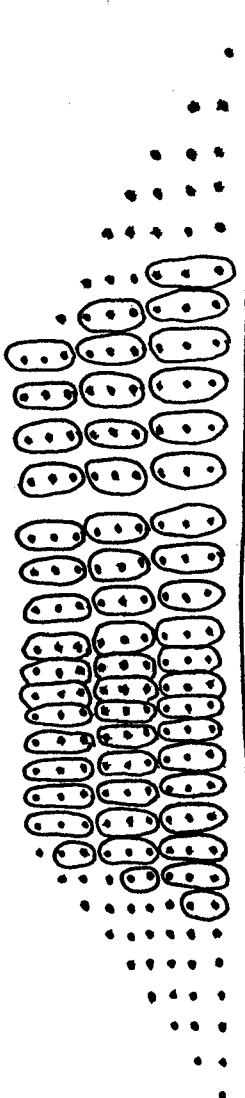
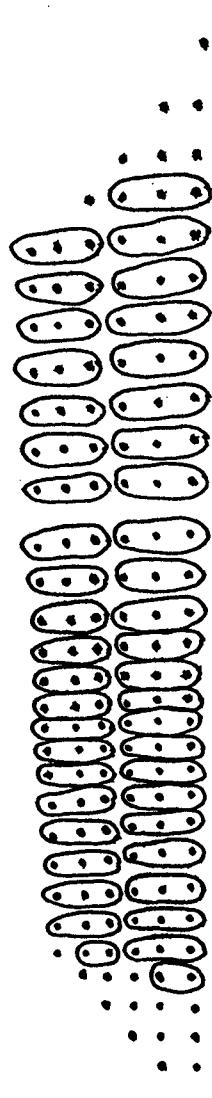
(5) K

8 FA
4 HA

40 FA
4 HA

51 FA
3 HA

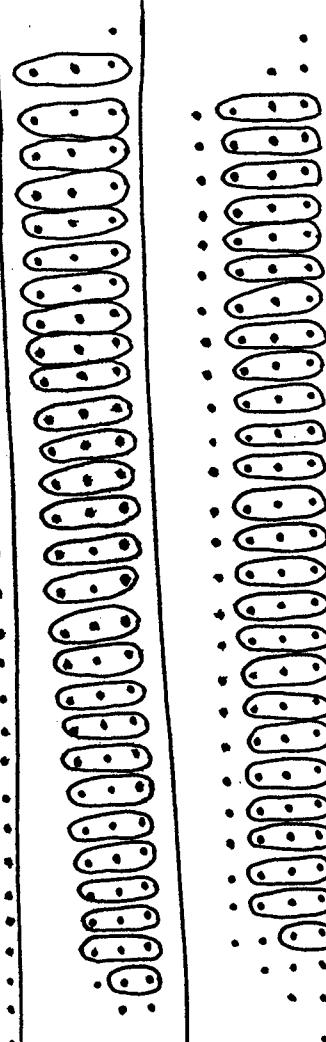
44 FA
2 HA



(6) ~~K~~

25 FA
1 HA

27 FA
1 HA



Total hardware = 195 FA and 15 HA

Delay = $6 \times D_{FA}$

(7) k

- The cost and delay for the previous 16 down to 2 reduction is:

$$\text{cost} = 195 \text{ FAs} + 15 \text{ HAs}$$

$$\text{delay} = 6 \times D_{FA}$$

where

D_{FA} = delay through full adder

- If an unsigned cellular array multiplier had been involved to perform the same task, the cost and delay would have been

$$\text{cost} = 15 \text{ HA} + 15 \times 14 \text{ FA}$$

$$= 15 \text{ HA} + 210 \text{ FA}$$

$$\text{delay} = D_{HA} + 14 \times D_{FA}$$

where

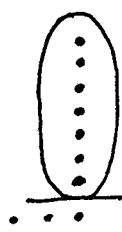
D_{HA} = delay through half adder;

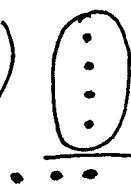
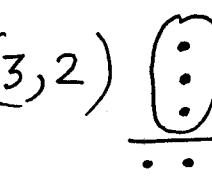
D_{FA} = delay through full adder.

(8) k

Problem 2: Reduce the matrix of summands of a 15-by-15 unsigned multiplier down to two rows using counters with largest counter being the (7,3) counter. This means that you cannot use (m,k) counters with $m > 7$ but you can use (m,k) counters with $m \leq 7$.

Solution: The counters allowed to be used are:

$(7,3)$  ; $(6,3)$  ; $(5,3)$ 

$(4,3)$  ; $(3,2)$  and $(2,2)$ 

The reduction will involve a 2-level CSA(7,3) tree performing the 15 down to 3 reduction followed by a CSA(3,2) performing the 3 down to 2 reduction.

(9) k

For minimum delay CSA(7,3) trees, the maximum # of operands $\Theta(v)$ to be added and reduced down to three (3) by a v-level tree are given by:

$$\left. \begin{aligned} \Theta(v) &= \left\lfloor \frac{\Theta(v-1)}{3} \right\rfloor \times 7 + (\Theta(v-1)) \bmod 3 \\ \Theta(1) &= 7 \end{aligned} \right\} \quad (2)$$

The sequence of $\Theta(v)$'s obtained from (2) is

$$7, 15, 35, \dots$$

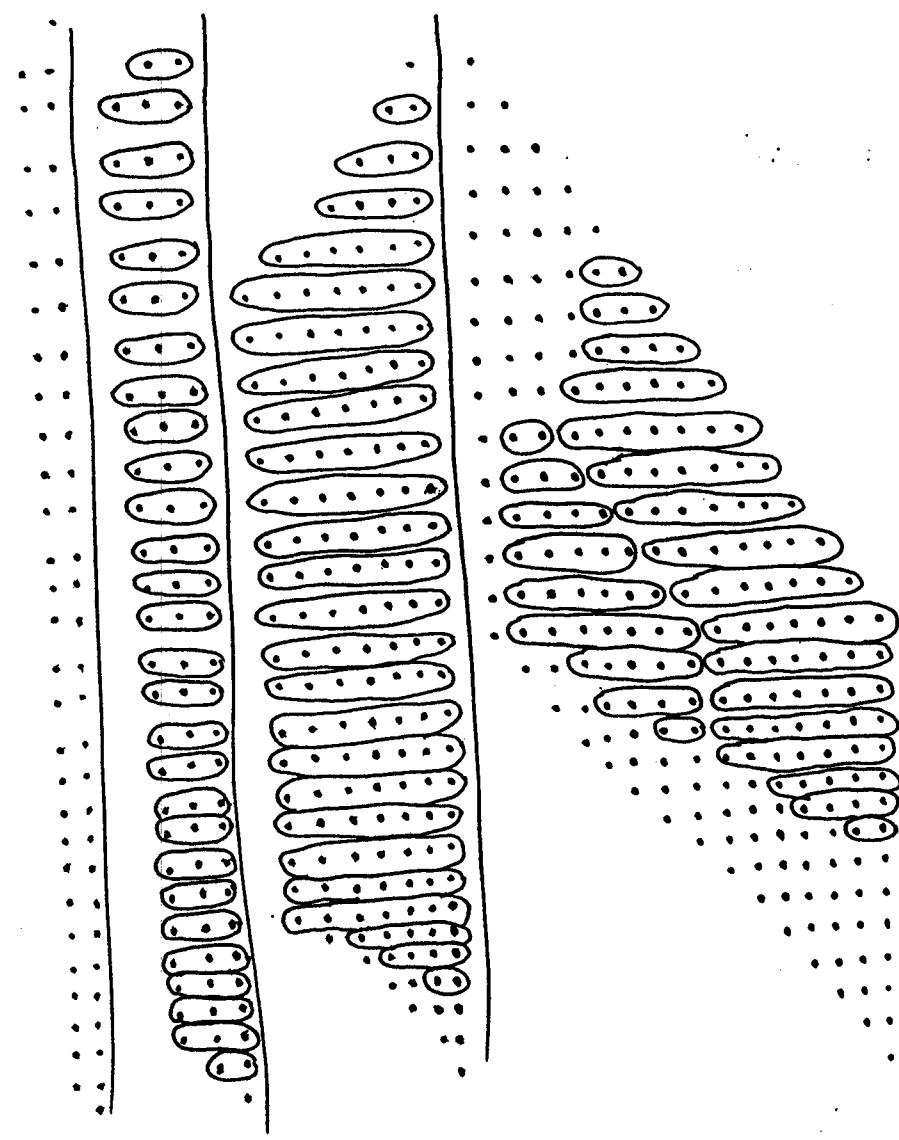
↑

Thus

1^{st} level is 15 down to 7 reduction } CSA(7,3)
 2^{nd} level is 7 down to 3 reduction } tree
 3^{rd} level is 3 down to 2 reduction } CSA(3,2).

The reductions follow on the next page.

(10) k



Problem 1: Construct a 16-by-16 unsigned multiplier using 4-by-4 unsigned multipliers, counters and one CPA. The largest counter allowed to be used is the (7,3) counter.

Solution : Let the two numbers to be multiplied be

$$A = \underbrace{a_{15} a_{14} a_{13} a_{12}}_{A_3} \underbrace{a_{11} a_{10} a_9 a_8}_{A_2} \underbrace{a_7 a_6 a_5 a_4}_{A_1} \underbrace{a_3 a_2 a_1 a_0}_{A_0} \quad ; \quad a_i \in \{0, 1\}$$

qnd

$$B = \underbrace{b_{15} b_{14} b_{13}}_{B_3} \underbrace{b_{12} b_{11} b_{10} b_9 b_8}_{B_2} \underbrace{b_7 b_6 b_5 b_4}_{B_1} \underbrace{b_3 b_2 b_1 b_0}_{B_0} ; \quad b_i \in \{0, 1\}$$

Then the product $P = A \times B$ is

$$x) \quad \begin{array}{l} A_3 \cdot 2^{12} + A_2 \cdot 2^8 + A_1 \cdot 2^4 + A_0 = A \\ B_3 \cdot 2^{12} + B_2 \cdot 2^8 + B_1 \cdot 2^4 + B_0 = B \end{array}$$

$$x) \quad B_3 \cdot 2^2 + B_2 \cdot 2^8 + B_1 \cdot 2^4 + B_0 = B$$

$$A_3 \cdot B_0 \cdot 2^{12} + A_2 \cdot B_0 \cdot 2^8 + A_1 \cdot B_0 \cdot 2^4 + A_0 \cdot B_0$$

$$A_3 \cdot B_1 \cdot 2^{16} + A_2 \cdot B_1 \cdot 2^{12} + A_1 \cdot B_1 \cdot 2^8 + A_0 \cdot B_1 \cdot 2^4$$

$$A_3 \cdot B_2 \cdot 2^2 + A_2 \cdot B_1 \cdot 2^2 + A_1 \cdot B_1 \cdot 2^2 + A_0 \cdot B_1 \cdot 2^2$$

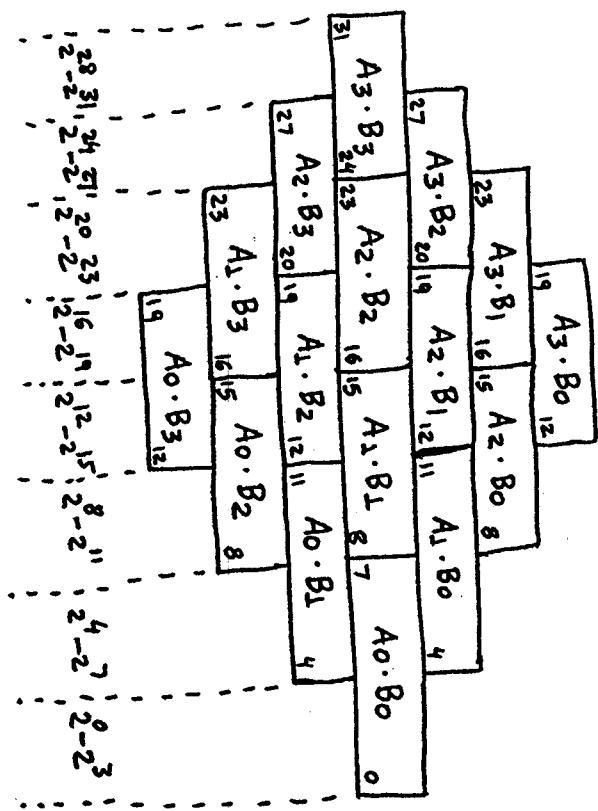
$$A_3 \cdot B_2 \cdot 2^{16} + A_2 \cdot B_1 \cdot 2^{12} + A_1 \cdot B_1 \cdot 2^8 + A_0 \cdot B_1 \cdot 2^8$$

$$A_3 \cdot B_1 \cdot 2 + A_2 \cdot B_1 \cdot 2 + A_1 \cdot B_1 \cdot 2 \\ A_3 \cdot B_2 \cdot 2^0 + A_2 \cdot B_2 \cdot 2^{16} + A_1 \cdot B_2 \cdot 2^{12} + A_0 \cdot B_2 \cdot 2^8$$

$$A_3 \cdot B_1 \cdot 2 + A_2 \cdot B_1 \cdot 2 + A_1 \cdot B_1 \cdot 2 \\ A_3 \cdot B_2 \cdot 2^0 + A_2 \cdot B_2 \cdot 2^{16} + A_1 \cdot B_2 \cdot 2^{12} + A_0 \cdot B_2 \cdot 2^8$$

$$A_3 \cdot B_3^{24} + A_2 \cdot B_3^{20} + A_1 \cdot B_3^{16} + A_0 \cdot B_3^{12}$$

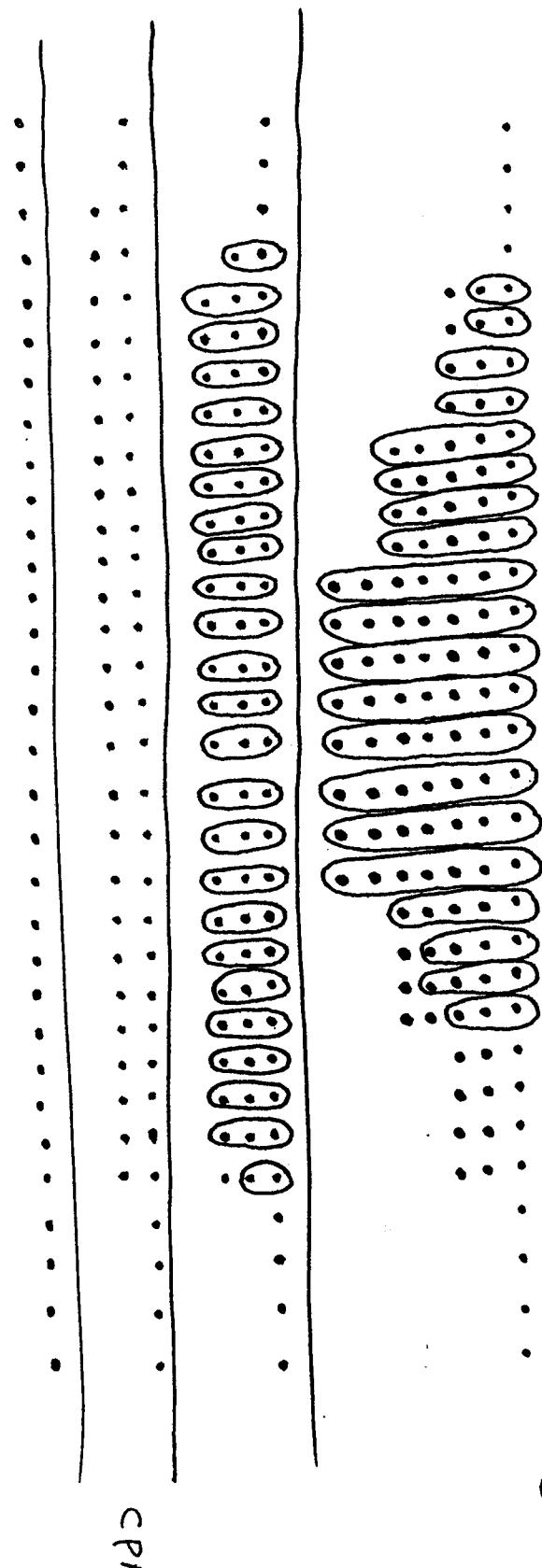
(2) e



Arrangement of the sixteen 8-bit products for the
16-by-16 unsigned multiplier.

(2)

(3)



The first level of reduction was a 7 down to 3 reduction and the largest counter used was the $(7, 3)$ counter. The second level was a 3 down to 2 reduction and $(3, 2)$ and $(2, 2)$ counters were used.

(4) l

Problem 2: Using nine 3-by-2 unsigned multipliers, (3,2) and (2,2) counters and one CPA, construct a 9-by-6 unsigned multiplier. Minimum number of counters should be used. The useful sequence of numbers is 3, 4, 6, 9, 13, 19, ...

Solution: Let A and B be 9-bit and 6-bit unsigned numbers respectively.

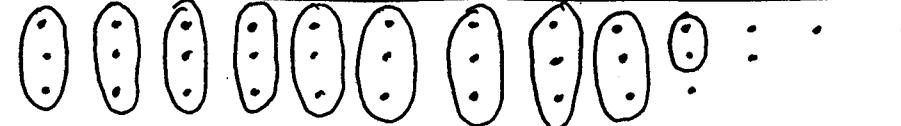
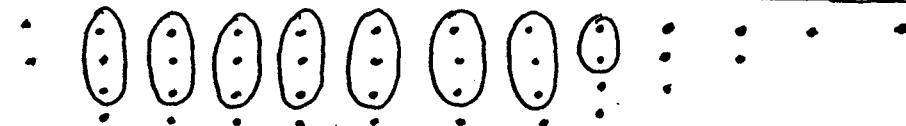
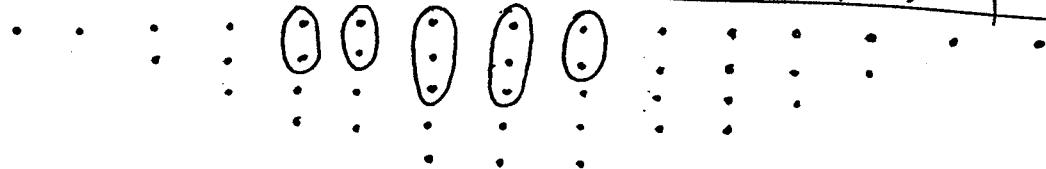
$$A = \underbrace{a_8 a_7 a_6}_{A_2}, \underbrace{a_5 a_4 a_3}_{A_1}, \underbrace{a_2 a_1 a_0}_{A_0} = A_2 2^6 + A_1 2^3 + A_0$$

$$B = \underbrace{b_5 b_4}_{B_2}, \underbrace{b_3 b_2}_{B_1}, \underbrace{b_1 b_0}_{B_0} = B_2 2^4 + B_1 2^2 + B_0$$

$$\begin{aligned} & A = A_2 2^6 + A_1 2^3 + A_0 \\ \times) \quad & B = B_2 2^4 + B_1 2^2 + B_0 \\ \hline & A_2 B_0 2^6 + A_1 B_0 2^3 + A_0 B_0 \\ & A_2 B_1 2^8 + A_1 B_1 2^5 + A_0 B_1 2^2 \\ & A_2 B_2 2^{10} + A_1 B_2 2^7 + A_0 B_2 2^4 \end{aligned}$$

(5) l

	2^{14}	2^{13}	2^{12}	2^{11}	2^{10}	2^9	2^8	2^7	2^6	2^5	2^4	2^3	2^2	2^1	2^0
$A_0 B_0$											x	x	x	x	
$A_0 B_1 2^2$										x	x	x	x	x	
$A_1 B_0 2^3$								x	x	x	x	x			
$A_0 B_2 2^4$							x	x	x	x	x				
$A_1 B_1 2^5$						x	x	x	x	x					
$A_2 B_0 2^6$					x	x	x	x	x						
$A_1 B_2 2^7$			x	x	x	x	x	x	x						
$A_2 B_1 2^8$		x	x	x	x	x									
$A_2 B_2 2^{10}$	x	x	x	x	x										



CPA

