

## Conference Proceedings and Technical Presentations

### Conference Proceedings:

1. P.K. Ajmera and J.R. Hauser, "Fabrication of ion-implanted  $(\text{Ga}_x\text{In}_{1-x})\text{As}$  PN junction diodes," Paper D.2 presented at Electronics of Compound Semiconductor Interfaces Conference, Feb. 5, 1974, Colorado State University, Report SF2, pp. 50-52, Office of Naval Research and Army Research Office, 1974.
2. P.K. Ajmera, Y.C.R. Kwor and M. Larkin, "A new approach to improving the reliability of communication in adverse weather in people mover transportation systems," in *Proceedings of the National Electronics Conference*, (1981), vol. 35, pp. 597-601.
3. Y.C.R. Kwor and P.K. Ajmera, "Radio frequency deicing of collector rails for AGT systems," *Transportation Research Record* 882, pp. 1-6, 1982.
4. P.K. Ajmera and W.J. Gajda, Jr., "Faculty research and its role in the undergraduate engineering education," in *IEEE/ASEE 12th Annual Conf. Proceedings*, (October 18, 1982), pp. 35-40.
5. P.K. Ajmera, "The use of computer in classroom for undergraduate instruction in microelectronics," in *Proc. of 5th Biennial University/Government/Industry Microelectronics Symposium*, (Texas A&M University, College Station, Texas, May 1983), pp. 90-93.
6. P.K. Ajmera and P.F. Haubursin, "Deposition of semiconductor GaAs by thick film technique," in *34th Electronic Components Conference Proceedings*, (New Orleans, LA, May 16, 1984), pp. 418-422.
7. P.K. Ajmera, "Vacuum growth of thin films of  $\text{ZnSnP}_2$ ," in *Meeting Abstracts Polycrystalline Thin Film Review Meeting*, (Solar Energy Research Institute, Golden, CO, Oct. 26, 1984), vol. SERI/CP-211-2548, pp. 235-245.
8. M. Mehta, V. Desireddi and P.K. Ajmera, "Data path components for RISC II using CMOS circuitry," in *6th Biennial UGIM Symposium Proceedings*, (Auburn University, Auburn, AL, June 11-13, 1985), pp. 171-176.
9. H. Rangchi, B. Huner, and P.K. Ajmera, "A model for deposition of thick films by the screen printing technique," Poster Paper T7, International Society for Hybrid Microelectronics Symp., in *Proc. 1986 Int'l. Symp. on Microelectronics*, (Atlanta, GA, Oct. 6-8, 1986), pp. 604-609 ("**Best Paper of Session**" award).
10. F. Mehrad and P.K. Ajmera, "Modeling of devices with position-dependent material properties," in *IEEE Southeast Conference Proc.*, (Tampa, FL, April 1987), pp. 176-181.
11. B. Huner, H. Rangchi and P.K. Ajmera, "On the release of the printing screen from the substrate in the breakaway region," in *Proc. of the 1987 Int'l. Symp. on Microelectronics*, (Minneapolis, MN, Sept. 28-30, 1987), pp. 328-334.
12. D.P. Norton and P.K. Ajmera, "Photochemical vapor deposition of GaAs for photovoltaics," in *Technical Digest - 3rd Int'l. Photovoltaic Science and Engineering Conference*, (Tokyo, Japan, Nov. 3-6, 1987), pp. 537-540.
13. P.K. Ajmera, "Physics and technology of thin film polycrystalline solar cells," in *Proc. of the 4th Int'l. Workshop on Physics of Semiconductor Devices*, (Madras, India, 1987), S.C. Jain and S. Radhakrishna, Ed., pp. 486-495 (**Invited**).
14. M.A. Haque and P.K. Ajmera, "Vacuum growth of uniform semiconductor films on large area flat substrates for photovoltaics," in *Proceedings of IEEE Southeastcon 1988*, (Knoxville, TN, April 10-13, 1988), pp. 234-238.
15. B. Huner, P.K. Ajmera and H. Rangchi, "The fluid mechanical stability of the screen printing process," in *Proceedings of the Int'l. Soc. for Hybrid Microelectronics 1988*, (Seattle, WA, Oct. 17, 1988), pp. 84-87.
16. D.P. Norton and P.K. Ajmera, "Photochemical vapor deposition of gallium arsenide," *SPIE*, vol. 945, pp. 55-61, 1988.
17. D.P. Norton and P.K. Ajmera, "Mass spectroscopic study of photolytically driven deposition of gallium arsenide," in *Proceedings of the IEEE Southeastcon 1989*, (Columbia, SC, April 9-12, 1989), vol. 1, pp. 351-356.
18. M. Madani and P.K. Ajmera, "Electrical properties of the oxide grown on Si at room temperature using point to plane corona discharge," in *Proceedings of the IEEE Southeastcon 1989*, (Columbia, SC, April 9-12, 1989), vol. 1, pp. 357-360.
19. P.K. Ajmera, J.D. Scott, B.C. Craft and R.A. Kinney, "Center for Advanced Microstructures and Devices at Louisiana State University for X-ray lithography research," in *8th Biennial UGIM Symposium Proceedings*, (Westborough, MA, June 12-14, 1989), pp. 76-79.

20. R.L. Stockbauer, P. Ajmera, E.D. Poliakoff, B.C. Craft and V. Saile, "A new synchrotron light source at Louisiana State University's Center for Advanced Microstructures and Devices (CAMD)," in *6th National Conference on Synchrotron Radiation Instrumentation*, (Berkeley, CA, August 1989).
21. B. Huner and P.K. Ajmera, "The leveling of a purely viscous printing ink on a rough substrate," in *Proceedings of Int'l. Symposium on Microelectronics*, (Oct. 24-26, 1989), pp. 561-567 ("**Best Paper of Session**" award).
22. M.R. Madani and P.K. Ajmera, "Interface properties of the oxide grown on Si at low temperatures using point to plane corona discharge," in *Proceedings of IEEE Southeast Conference*, (New Orleans, LA, April 1-4, 1990), pp. 992-995.
23. A.K. Dutta and P.K. Ajmera, "Characterization of defects in semiconductor materials by infrared piezo-birefringence," in *Proceedings of IEEE Southeast Conference*, (New Orleans, LA, April 1-4, 1990), pp. 1066-1071.
24. M.R. Madani and P.K. Ajmera, "Application of Nicollian-Reisman model to negative point-to-plane corona oxidation of silicon," in *Proc. of IEEE Southeastcon 1991*, (Williamsburg, VA, April 7-10, 1991), pp. 388-391.
25. G.S. Kousik, Z.G. Ling and P.K. Ajmera, "Measurement of bulk lifetime and surface recombination velocities at the two surfaces by infrared absorption due to pulsed optical excitation," in *22nd IEEE Photovoltaic Specialists Conference*, (Las Vegas, NV, Oct. 7-11, 1991), pp. 246-249.
26. G.L. Griffin, W. Lai, A.W. Maverick, R. Kumar and P.K. Ajmera, "Reaction Kinetics of CVD Copper," in *Advanced Metallization for VLSI Application*; R.V. Joshi ed.; Materials Research Society, (Pittsburg, PA, 1992), pp. 367-373.
27. A. Misra and P.K. Ajmera, "Effects of hydrogen annealing on low temperature grown oxides on silicon by negative point-to-plane corona discharge oxidation," in *Proc. of IEEE Southeast Conference*, (Birmingham, AL, April 12-15, 1992), pp. 167-170.
28. Z.G. Ling, P.K. Ajmera and G.S. Kousik, "Nondestructive Measurement of Bulk Lifetime and Surface recombination Velocities," in *23rd IEEE Photovoltaic Specialists Conference* (Louisville, Kentucky, May 10-14, 1993), pp. 360-363.
29. P.K. Ajmera and A.H. Marshak, "Integrating classroom and laboratory experiences in the undergraduate engineering curriculum," in *1994 ASEE Proceedings Gulf Southwest Section Meeting*, (Baton Rouge, LA, March 24-25, 1994), pp. 117-123.
30. J. Song, G.S. Lee and P.K. Ajmera, "Chemical and electrical characterization of low temperature plasma enhanced CVD silicon oxide films using  $\text{Si}_2\text{H}_6$  and  $\text{N}_2\text{O}$ ," in *Proc. International Conference on Thin Films*, (San Diego, CA, April 1995).
31. S. Stadler and P.K. Ajmera, "Integrated acceleration sensors compatible with the standard CMOS fabrication process," in *Proc. of Int'l. Soc. of Hybrid Microelectronics*, (Los Angeles, CA, October 24-26, 1995), pp. 95-100.
32. G. Nallapati and P.K. Ajmera, "Plasma enhanced chemical vapor deposition of silicon nitride films using disilane as silicon source: Some process characterization details," in *Proc. of 2nd Workshop of Science and Technology*, (CAMD, Baton Rouge, LA, April 8, 1996).
33. S. Stadler and P.K. Ajmera, "Integration of post-fabricated mechanical parts on a chip with standard CMOS electronic components," in *Proc. of 2nd Workshop of Science and Technology*, (CAMD, Baton Rouge, LA, April 8, 1996).
34. S. Stadler and P.K. Ajmera, "Integration of LIGA structures with CMOS circuitry," in *Proc. of 4th Annual SPIE Symp. on Smart Structures and Materials*, (San Diego, CA, March 6, 1997), vol. 3046, pp. 230 - 241 (**Invited**).
35. P. K. Ajmera, S. Stadler, C. Khan Malek and V. Saile, "High-aspect ratio structures for sensors compatible with electronic circuitry," in *Physics of Semiconductor Devices*, vol. 1, Eds. V. Kumar and S.K. Agarwal, 9th International Workshop on Physics of Semiconductor Devices, ( Delhi, India, Dec. 16-20, 1997), pp 504 - 508.
36. G. Nallapati and P. K. Ajmera, "Low temperature plasma enhanced chemical vapor deposition of silicon nitride and oxynitride layers," in *Physics of Semiconductor Devices*, vol. 1, Eds. V. Kumar and S. K. Agarwal, 9th International Workshop on Physics of Semiconductor Devices, (Delhi, India, Dec.16-20, 1997), pp 573 - 579. (**Invited**).
37. A. Srivastava, S.V. Prasanna and P.K. Ajmera, "Readout electronics scheme in CMOS technology for integration with analog outputs from integrated smart sensors," in *Proc. of 5th Annual International SPIE Symp. on Smart Structures and Materials*, (San Diego, CA, March 2-5, 1998), vol. 3328, pp 274 - 284.

38. P.K.Ajmera, S. Stadler and N. Abdollahi, "Development of a low-cost x-ray mask for high-aspect ratio MEM smart structures," in *Proc. of 5th Annual International SPIE Symp. on Smart Structures and Materials*, (San Diego, CA, March 2-5, 1998), vol. 3328, pp 14 - 22 . **(Invited)**.
39. G. S. Lee, Y. Y. Jin, S. J. Park, P. K. Ajmera, C. Khan Malek, J.T. Wang and F. Tang, "A LIGA-like process for high-aspect ratio PZT microstructures," in *Proc. of 6th Annual International SPIE Symp. On Smart Structures and materials*, (Newport Beach, CA, March 1-3, 1999), vol. 3673, pp 127 - 132.
40. A. Srivastava, H. Yong, D. Wildhaber, M. Hassan, V. Gongalreddy, J. Wang, P. K. Ajmera and F. Pournalborz, "A digital readout CMOS design for capacitive sensors using on-chip variable sense capacitors arrays," in *Proc. of 6th Annual International SPIE Symp. On Smart Structures and Materials*, (Newport Beach, CA, March 1-3, 1999), vol. 3673, pp 93 - 103.
41. P. K. Ajmera, X. Wang, J. Ross, G. Nallapati and H. Manohara, "A new technique for direct integration of mechanical motion with electronics on a chip," in *Proc. 7th Annual Intl. Symp. on Smart Structures and Materials*, Newport Beach, CA, March 6-8, 2000, vol. 3990, pp 28 - 35, 2000.
42. P. K. Ajmera, "System Integration," Proc. of 1st Annual Louisiana Microsystems Conf., Ruston, LA, April 5, 2000 pp 1-5 **(Invited)**.
43. P. K. Ajmera and In-Hyook Song, "Laterally Movable Gate FET (LMGFET) for On-Chip Integration of MEMS with Electronics," SPIE's 8th International Symposium on Smart Structures and Materials – Smart Electronics and MEMS (ss09), Newport Beach, CA, March 5-7, 2001, vol. 4334, pp 30 – 37, 2001.
44. In-hyook Song, Tinghui Xin and Pratul K. Ajmera, "Laterally Movable Gate Field Effect Transistor (LMGFET) for Microsensor and Microactuator Applications," In V. Kumar and P. K. Basu Eds., Proc. of 11th International Workshop on the Physics of Semiconductor Devices, New Delhi, India, Vol 1, pp 468 – 474, Dec. 11-15, 2001 **(Invited)**.
45. A. Srivastava, H. Venkata and P. K. Ajmera, "A novel scheme for a higher bandwidth sensor readout," SPIE's 9th Intl. Symp. On Smart Structures and Materials – Smart Electronics, MEMS and Nanotechnology (ss0), San Diego, March 18-20, 2002, vol. 4700, pp 17-28, 2002.
46. In-hyook Song, T. Xin and P. K. Ajmera, "Laterally movable gate FET (LMGFET) as a resonant gate device," SPIE's 9th Intl. Symp. On Smart Structures and Materials – Smart Electronics, MEMS and Nanotechnology, San Diego, March 18-20 (ss0), 2002 vol. 4700, pp 29 – 39, 2002
47. C. Zhang, A. Srivastava and P. K. Ajmera, "A 0.8 V ultra-low-power CMOS operational amplifier design," Proceedings of 45th Mid-West Symp. on Circuits and Systems, Aug. 4-7, 2002, Tulsa, OK, Vol. 1, pp 9 –12, 2002. **(10 Best Student Paper Award)**.
48. In-hyook Song, Sunitha Kopparthi, P. K. Ajmera and A. Srivastava, "Design, Simulation and Fabrication of a Novel Integrated Micro-Accelerometer Utilizing a Post-CMOS Fabrication Technique," SPIE Smart Structures and Materials Conference, March 2-6, San Diego, SPIE vol. 5055, pp 78-86, 2003.
49. C. Zhang, Tinghui Xin, Ashok Srivastava\* and Pratul K. Ajmera, "Ultra low-power operational amplifier CMOS chip for monolithic integration with neural microprobes," SPIE Smart Structures and Materials Conference, March 2-6, San Diego, SPIE vol. 5055, pp 29-35, 2003.
50. C. Zhang, A. Srivastava and P. K. Ajmera, "Noise analysis of a 0.8 V ultra low-power CMOS operational amplifier," SPIE 1st Intl. Symp. on Fluctuations and Noise, June 1-4, 2003, Santa Fe, New Mexico. In SPIE Proc. Vol. 5113, pp 294 – 300, 2003.
51. In-hyook Song, Pratul K. Ajmera and Sunitha Kopparthi, "Integrated Microsystem utilizing Laterally Movable Gate FETs," Proc. of 11th International Workshop on the Physics of Semiconductor Devices, IIT-Chennai, India, Eds: K. N. Bhat and A. Dasgupta, Vol 2, pp. 631-636, Dec. 17-20, 2003 **(Invited)**.
52. In-hyook Song and Pratul K. Ajmera, "Differential Laterally Movable Gate FETs (LMGFETs) as a Position Sensor," In Proc. of SPIE 11th Annual Intl. Symp. on Smart Structures and Materials, Vol.5389, pp 267-273, March 15-18, San Diego, CA, 2004.
53. Sunitha Kopparthi and Pratul K. Ajmera, "Power Delivery for Remotely Located Microsystems," Proc. of 2004 IEEE Region 5 Annual Technical & Leadership Workshop, April 2-4, 2004, pp 31-39, Norman, OK, April 2004.
54. C. Zhang, A. Srivastava and P. K. Ajmera, "0.8 V ultra low-power CMOS analog multiplexer for remote biological and chemical signal processing," In Proc. of SPIE 11th Annual Intl. Symp. on Smart Structures and Materials, Vol.5389, pp 13-19, March 15-18, San Diego, CA, 2004.
55. T. Xin, P. K. Ajmera, C. Zhang and A. Srivastava, "High-aspect ratio neural probes for monolithic integration with ultra-low power CMOS operational amplifier circuit," In Proc. of SPIE 11th Annual Intl. Symp. on Smart Structures and Materials, Vol.5389, pp 20-25, March 15-18, San Diego, CA, 2004.

56. Tinghui Xin, Pratul K. Ajmera, Chuang Zhang, and Ashok Srivastava, "Post-CMOS Chip-Level Processing for High-Aspect Ratio Microprobe Fabrication Utilizing Pulse Plating," In Proc. of SPIE 12<sup>th</sup> Annual Intl. Symp. on Smart Structures and Materials, Vol. 5763, pp 1 – 10, March 7-10, San Diego, CA, 2005.
57. Raghavendra Anantha, Ashok Srivastava and Pratul Ajmera "Charge Pump CMOS Circuit Based on Internal Clock Voltage Boosting for Bio-Medical Applications," In Proc. of SPIE 12<sup>th</sup> Annual Intl. Symp. on Smart Structures and Materials, Vol. 5763, pp 11 – 19, March 7-10, San Diego, CA, pp 2005.
58. V. Gaddam, J. Yernagula, R. Anantha, S. Kona, S. Kopparthi, P. Ajmera and A. Srivastava, "Remote Power Delivery for Hybrid Integrated Bio-implantable Electrical Stimulation System," In Proc. of SPIE 12<sup>th</sup> Annual Intl. Symp. on Smart Structures and Materials, Vol. 5763, pp 20 – 31, March 7-10, San Diego, CA, 2005.
59. Min Zhang, Franz-Josef Hormes, Pratul K. Ajmera, Kun Lian, Jost Goettert, "Superparamagnetic Particle Embedded Microprobe (SPEM) for GMR Sensor Sensitivity Calibration," Proceedings of the 2005 International Conference on MEMS, NANO and Smart Systems (ICMENS'05), IEEE Computer Society, pp 175-176, ISBN:0-7695-2398-6, INSPEC No: 8634177, DOI: 10.1109/ICMENS.2005.115, 2005.
60. T. Xin and P. K. Ajmera, "Nickel electrodeposition studies for high-aspect-ratio microstructure fabrication for MEMS" In Proc. of SPIE 13<sup>th</sup> Annual Intl. Symp. on Smart Structures and Materials, Vol.6172, pp OE1-OE9, Feb. 26 – Mar. 1, San Diego, CA, 2006.
61. In-hyook Song and P. K. Ajmera, "A Laterally Movable Gate Field Effect Transistor for Sensors and Actuators: Design, Fabrication and Test Results," TEXMEMS VIII, in Conference Extended Abstracts Proceedings, University of Texas – Dallas, Richardson, TX, Oct. 9, 2006.
62. M. Zhang, D. Brignac, P. Ajmera and K. Lian, "A low-frequency vibration-to-electrical energy harvester," In Proc. of SPIE 15<sup>th</sup> Annual Intl. Symp. On Smart Structures and Materials, Nanosensors and Microsensors for Bio-Systems 2008, Vol. 6931, pp OS1-OS12, March 11-13, 2008, San Diego, CA, 2008.
63. Naga S. Korivi and Pratul K. Ajmera, "Micropatterning of biocompatible materials," International Conference on MEMS (ICMEMS 2009), Indian Institute of Technology-Madras, Chennai, INDIA, Conf. Proc. pp 1-6, January 3-5, 2009, **(Invited)**.
64. Kyung-Nam Kang, Yoonyoung Jin, P. K. Ajmera and S. Park, "Low temperature deposition of carbon nanotubes," In Proc. of SPIE 16<sup>th</sup> Annual Intl. Symp. On Smart Structures and Materials, Nanosensors and Microsensors for Bio-Systems 2009, Vol. 7291, pp 729112-1 to 729112-8, March 10, 2009, San Diego, CA, 2009.
65. N.S. Korivi, and P.K. Ajmera, "Percutaneous access device with stress relief features," *Proceedings of ASME 5<sup>th</sup> Frontiers in Biomedical Devices Conference & Exhibition*, Newport Beach, California, September 20-21, 2010, paper no: BioMed2010-32022.
66. N.S. Korivi, C. Halliburton, and P.K. Ajmera, "Thermally responsive structures for gastric pacing and other applications," *Proceedings of ASME 5<sup>th</sup> Frontiers in Biomedical Devices Conference & Exhibition*, Newport Beach, California, September 20-21, 2010, paper no: BioMed2010-32074.

### Technical Presentations at Organized Conferences:

1. P.K. Ajmera and J.R. Hauser, "Growth of variable band gap (Ga<sub>x</sub>In<sub>1-x</sub>)As by vapor phase epitaxy," presented at the Symposium on Infrared Materials and Devices, Solid State Physics Laboratory, Delhi, India, March 11-12, 1976.
2. Y.C.R. Kwor and P.K. Ajmera, "Radio frequency deicing of collector rails for AGT systems," presented at the 61st Meeting of Transportation Research Board, National Research Council, Washington D.C., Jan. 1982.
3. P.K. Ajmera, "Microcomputers as a classroom teaching aid," presented at the 1982 Meeting of the National Science Teachers Association and Society of College Science Teachers, SCST Annual Meeting Program Abstracts, vol. 2, pp. 25, April 1, 1982.
4. P.K. Ajmera and H. Shin, "Growth and characterization of thin films of ZnSnP<sub>2</sub>," Poster Paper, presented at the 7th Photovoltaic Advanced Research & Development Project Review Meeting, Solar Energy Research Institute, Meeting Abstracts, pp. 97, SERI/CP-210-2927, Denver, CO, May 13-15, 1986.
5. D.P. Norton and P.K. Ajmera, "Characterization of polycrystalline GaAs deposited by light-assisted chemical vapor deposition," presented at the 1st Annual Materials Science and Engineering Technical Paper Session," Birmingham, AL, Sept. 30, 1987.

6. Dutta, P.K. Ajmera and B. Huner, "Characterization of defects in semiconductor materials using infrared piezo-birefringence," Poster Paper Session I, present at the 4th Int'l. Workshop on the Electron Devices, Circuits and Systems, IIT, Kharagpur, India, December 16, 1987.
7. P.K. Ajmera and D.P. Norton, "Low-temperature photochemical vapor deposition of GaAs on synthetic fused silica," presented at the Int'l. Symp. on Electron Devices, Circuits and Systems at IIT, Kharagpur, India, Dec. 16, 1987.
8. D.P. Norton and P.K. Ajmera, "Photochemical vapor deposition of GaAs," presented at the SPIE Conf. on Advances in Semiconductors and Superconductors: Physics and Device Applications, Technical Abstracts, pp. 59, Newport Beach, CA, March 13-18, 1988.
9. P.K. Ajmera, "Thin film growth techniques: MBE and CVD," presented at the 16th Annual Symp. on Molecular Electronic Structure and Spectroscopy, LSU, Baton Rouge, LA, May 19-20, 1988.
10. D.P. Norton and P.K. Ajmera, "Excimer laser and Hg-Xe arc lamp driven low temperature deposition of gallium arsenide," presented at the 1989 Conference on Lasers and Electro-Optics (CLEO), Baltimore, Maryland, April 28, 1989.
11. R.L. Stockbauer, E.D. Poliakoff, P. Ajmera, B.C. Craft, and V. Saile, "Center for Advanced Microstructures and Devices (CAMD) at Louisiana State University," presented at the 9th Int'l. Conf. on Vacc. UV Radiation Physics, Honolulu, Hawaii, July 1989.
12. N.Abdollahi, J. Ross and P. K. Ajmera, "A Low-cost X-ray mask for high-aspect ratio structures," Poster Paper, 5<sup>th</sup> CAMD Science & Technology Symposium and Users' Meeting, Baton Rouge, May 7-8, 1999.
13. J. Ross, P. K. Ajmera and H. Manohara, "Fabrication of a novel device to integrate mechanical motion with electronics," Poster paper presented at 1<sup>st</sup> Louisiana Microsystems Conf., Ruston, LA, April 5, 2000.
14. J. Ross, P. K. Ajmera and H. Manohara, "Fabrication of a novel device to integrate mechanical motion with electronics," Poster paper presented at 6<sup>th</sup> CAMD Science & Technology Symp. and Users' Meeting, April 7, 2000.
15. J. Gottert, G. Aigeldinger, P. Ajmera, K. Kelly, K. Lian, Z. G. Ling, H. Manohara, M. Murphy, E. Podlaha and S. Soper, "High Aspect Ratio Microfabrication at Louisiana State University," TEXMEMSII, Abstracts 2<sup>nd</sup> Texas workshop on MEMS, p 11, Dallas, Texas, May 16, 2000.
16. In-Hyouk Song and Pratul Ajmera, "Direct-coupling of mechanical motion with on-chip electronics," Poster paper #2 presented at 7<sup>th</sup> CAMD Science & Technology Symposium and Users' Meeting, April 20, 2001.
17. Pratul K. Ajmera, "CAMD Users' Committee Report," 7<sup>th</sup> CAMD Science & Technology Symposium and Users' Meeting, April 20, 2001. **(Invited)**
18. In-hyouk Song and P. K. Ajmera, "Design and fabrication of laterally movable gate FET (LMGFET) for MEMS sensors and actuators," 2<sup>nd</sup> Louisiana Conference on Microfabrication and Materials Science, Baton Rouge, Aug. 20-22, 2001. **(Best Student Poster Paper Award - Microfabrication)**.
19. In-hyouk Song, T. Xin and P. K. Ajmera, "Modeling and Simulation for Laterally Movable Gate FETs As Audio Frequency Resonators." Poster paper, 8<sup>th</sup> CAMD Science & Technology Symposium and Users' Meeting, April 19, 2002.
20. R. Dust, D. Yemane, P. Pasupuleti, A. Bhusan, M. C. Murphy, K. R. Carney, E. B. Overton, J. Goettert and P. K. Ajmera, "Fabrication of a LIGA Gas Chromatograph Column As a Step Towards a Microfabricated GC," Poster paper, 8<sup>th</sup> CAMD Science & Technology Symposium and Users' Meeting, April 19, 2002.
21. In-Hyouk Song and P. K. Ajmera, "Modeling and Simulation of LMGFET Structures for Audio Frequency Spectrum Analysis," 3<sup>rd</sup> Louisiana Microsystems Conference – Louisiana Conf. On Commercial Applications of Microsystems, Materials and Nanotechnologies, Ruston, LA, Oct. 21-22, 2002.
22. Satish Kona and Pratul Ajmera, "Circuitry for Remote Charging of Bio-Implanted Batteries," Poster Paper, 3<sup>rd</sup> Louisiana Microsystems Conference – Louisiana Conf. On Commercial Applications of Microsystems, Materials and Nanotechnologies, Ruston, LA, Oct. 21-22, 2002.
23. C. Zhang, T. Xin, A. Srivastava and P. K. Ajmera, "An Integrated CMOS Ultra-Low Power Amplifier and Neural Microprobe Array for Detection of Biological Signals," 3<sup>rd</sup> Louisiana Microsystems Conference – Louisiana Conf. On Commercial Applications of Microsystems, Materials and Nanotechnologies, Ruston, LA, Oct. 21-22, 2002.
24. In-Hyouk Song, S. Kopparthi, P. K. Ajmera and A. Srivastava, "Laterally Movable Gate FET (LMGFET) Accelerometer on a Chip," 9<sup>th</sup> CAMD Science & Technology Symposium and Users' Meeting, April 25, 2003.
25. P. K. Ajmera, A. Srivastava, I. Song, C. Zhang, T. Xin, S. Kona, S. Kopparthi, J. Yernagula and A. Chamakura, "Microsystem integration – Imparting intelligence to sensors and actuators to fabricate smart sensors," NSF-EPSCoR Presentation, Capital Building, Baton Rouge, LA, April 29, 2003.

26. T. Xin and P.K. Ajmera, "Process details for a showerhead PECVD for thin films," Poster paper at the 7<sup>th</sup> Louisiana materials and Emerging Technologies Conference, Ruston, LA, Dec. 12-13, 2005.
27. M. Zhang, P. Ajmera, J. Goettert, K. Lian and J. Hormes, "Superparamagnetic particle embedded microphone (SPEM) for GMR sensor calibration," Poster paper at the 7<sup>th</sup> Louisiana materials and Emerging Technologies Conference, Ruston, LA, Dec. 12-13, 2005.
28. In-hyounk Song, Yoonyoung Jin and Pratul K. Ajmera, "Fabrication of a polymeric tapered HARMs array utilizing a low-cost nickel electroplated mold insert," High Aspect Ratio Micro Structure Technology Workshop (HARMST2005), June 10-13, Gyeongju, Korea, 2005.
29. B. Thiruvengadachari and P. K. Ajmera, "CVD growth of carbon nanotubes on oxidized silicon using cyclohexane precursor," 7<sup>th</sup> Louisiana Materials & Emerging Technologies Conference, in Conference Extended Abstracts Proceedings, Louisiana State University, Baton Rouge, LA, Oct. 23-24, 2006.
30. In-Hyounk Song, Kyung-Nam Kang, Yoonyoung Jin, D. S. Park and P. K. Ajmera, "Microlens Array Fabrication by Backside Exposure Using Fraunhofer Diffraction," HARMST 2007, Besancon, France, June 7-9, 2007.
31. Kyung-Nam Kang, Y. Jin, F. Dawan, J. Goettert and P. K. Ajmera, "Shape controllable micro-nozzle fabrication," HARMST 2007, Besancon, France, June 7-9, 2007.
32. N. Korivi and P. K. Ajmera, "Incorporation of micro- and nano-scale porosity on silicon surfaces," The 54<sup>th</sup> Intl. Conf. on Electron, Ion and Photon Beam Technology & Nanofabrication (EIPBN), Paper 5B.5, Session 5B-Emerging Technology 2, Anchorage, AL, June 1-4, 2010.
33. C. Halliburton, N.S. Korivi, and P.K. Ajmera, "Polyethylene glycol filled structures for gastric pacing applications," *Proceedings of the Louisiana Collegiate Honors Council Conference 2010*, Louisiana State University, Baton Rouge, LA, February 2010.
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35. J.Goettert, P. K. Ajmera, J. Kim, K.-N. Kang, Y. Jin, and D. Yemane, "Microfluidic Development Platform for BioSensor Applications," Annual ARMI Mardi Gras Review and Symposium, March 2011, New Orleans, LA.
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